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LOCATION OBJECT CODE LINE SOURCE LINE
1844 2004 JR NZ_CASE_OF_GEN10
1846 0E03 LD C,3 ;VALUE TO WRITE FOR VRAM ADDRESS OF 00H
1848 1828 JR INIT_TABLE90
184A CASE_OF_GEN10
184C 0E07 LD C,7 ;VALUE TO WRITE FOR VRAM ADDRESS OF 2000H
184E 1824 JR INIT_TABLE90
184E CASE_OF_COLOR
184E 0603 LD B,3 ;REGISTER TO WRITE
1850 7D LD A,L
1851 84 OR H
1852 2004 JR NZ_CASE_OF_CLR10
1854 0E7F LD C,7FH ;VALUE TO WRITE FOR VRAM ADDRESS OF 00H
1856 181A JR INIT_TABLE90
1858 CASE_OF_CLR10
1858 0E0F LD C,OFFH ;VALUE TO WRITE FOR VRAM ADDRESS OF 2000H
185A 1816 JR INIT_TABLE90
185C INIT_TABLE80
185C ;**
185C ;**
185C ;** COMPUTE BASE ADDRESS (BASE_ADDRESS=TABLE_ADDRESS/FACTOR
185C ;** GET BIT SHIFT COUNT
185C ;**
185C FD211876 LD IY,BASE_FACTORS
1860 FD09 ADD IY,BC
1862 FD09 ADD IY,BC
1864 FD7E00 LD A,(IY+0) ;SHIFT COUNT NOW IN 'A'
1867 FD4601 LD B,(IY+1) ;REGISTER NUMBER TO WRITE IN 'B'
186A DIVIDE ;** COMPUTE BASE ADDRESS
186A CB3C SRL H ;SHIFT HI BYTE
186C CB1D RRR L ;SHIFT LO BYTE
186E 30 DEC A ;DECREMENT SHIFT COUNT
186F 20F9 JR NZ,DIVIDE
1871 4D ;** WRITE TO VDP REGISTER
1872 ;** LD C,L ;VALUE TO WRITE IN 'C'
1872 CD1CCA CALL REG_WRITE
1875 C9 RET
1876
1876 BASE_FACTORS ;BASE_FACTOR,REGISTER_NUMBER
1876 070508060A DEFB 7,5,11,6,10,2,11,4,6,3
1878 0208040603
1880 00050001
1884 00010001
1888 FFFE0002
188C
188C 011880 LD BC,GET VRAM_P
188F 11738A LD DE,PARAM_AREA
1892 CD0098 CALL PARAM
1895 3A738A LD A,(PARAM_AREA)
1908 FD5B7400 LD DE,(PARAM_AREA+1)

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6069 * PROCEDURE GET_VRAMQ (TABLE_CODE:BYTE;START_INDEX:BYTE;SLICE:BYTE;
6070 * VAR DATA:BUFFER;ITEM_COUNT:INTEGER);
6071
6072 * THIS IS THE PASCAL ENTRY POINT TO INIT_TABLE
6073
6074 GET_VRAM_P DEFW 5,1,1,1,-2,2

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6075 * THIS IS THE PARAMETER DESCRIPTOR FOR INIT_TABLEQ
6076
6077 GET_VRAMQ
6078 LD BC,GET VRAM_P
6079 LD DE,PARAM_AREA
6080 CALL PARAM
6081 LD A,(PARAM_AREA)
6082 LD DE,(PARAM_AREA+1)

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LOCATION OBJECT CODE LINE SOURCE LINE
180E 2008 6140 JR Z,END_ADJ_COUNT
18E0 18E0 ADJUST_COUNT 6141 ADJUST_COUNT ;MULTIPLY ITEM COUNT TO GET BYTE COUNT
18E1 C821 SLA C
18E2 C810 RL B
18E4 30 6143 RL B
18E5 20F9 6144 DEC A
18E7 ED4373FE 6145 JR NZ,ADJUST_COUNT
18E8 18E8 LD [SAVED_COUNT],BC ;SAVE ADJUSTED COUNT
6146 END_ADJ_COUNT
6148
18E8 C1 6149 POP BC ;RESTORE TABLE_CODE/INDEX
18EC 6150 SET_COUNT20
18EC E5 6151 PUSH HL
18ED D009 6152 ADD IX,BC ;GET TABLE ADDRESS IN VRAM
18EF D009 6153 ADD IX,BC
18F1 D06E00 6154 LD L,[IX+0] ;LOW ORDER OF VRAM ADDRESS
18F4 D06601 6155 LD H,[IX+1] ;HIGH ORDER OF VRAM ADDRESS
18F7 19 6156 ADD HL,DE ;ADD BYTE INDEX TO GET VRAM START ADDRESS
18F8 EB 6157 EX DE,HL ;VRAM DESTINATION NOW IN DE
18F9 E1 6158 POP HL ;RESTORE DATA POINTER
18FA ED4873FE 6159 LD BC,[SAVED_COUNT] ;RESTORE ADJUSTED COUNT
18FE 6160 SET_COUNTX
18FE C9 6161 RET
6162
6163
18FF 6164 SHIFT_CT DEFB 2,3,0,3,3
18FF 0203000303 6165
6166
6168 * PROCEDURE PUT_VRAMQ (TABLE_CODE:BYTE;START_INDEX:BYTE;SLICE:BYTE;
6169 * VAR DATA:BUFFER;ITEM_COUNT:INTEGER);
6170
6171 * THIS IS THE PASCAL ENTRY POINT TO INIT_TABLE_
6172 PUT_VRAM_P DEFW 5,1,1,1,-2,2
6173
6174 * THIS IS THE PARAMETER DESCRIPTOR FOR INIT_TABLEQ
6175
6176 PUT_VRAMQ
6177 LD BC,PUT_VRAM_P
6178 LD DE,PARAM_AREA
6179 CALL PARAM
6180 LD A,[PARAM_AREA]
6181 LD DE,[PARAM_AREA+1]
6182 LD IY,[PARAM_AREA+5]
6183 LD HL,[PARAM_AREA+3]
6184
6185 PUT_VRAM_
6186 ;WRITES A CERTAIN NUMBER OF BYTES TO VRAM
6187 ;FROM A BUFFER.
6188 ;IN: TABLE CODE IN A
6189 ; 0=SPRITE NAME TABLE
6190 ; 1=SPRITE GENERATOR TABLE, 2=PATTERN NAME
6191 ; TABLE, 3= PATTERN GENERATOR TABLE, 4=
6192 ; COLOR TABLE
6193 ; START INDEX IN DE,
6194 ; DATA BUFFER IN HL, AND COUNT IN IY.
6195
1C04 00050001
1C08 00010001
1C0C FFFE0002
1C10
1C10 011C04 LD BC,PUT_VRAM_P
1C13 11738A LD DE,PARAM_AREA
1C16 CD0098 CALL PARAM
1C19 3A738A LD A,[PARAM_AREA]
1C1C ED587388 LD DE,[PARAM_AREA+1]
1C20 FD2A738F LD IY,[PARAM_AREA+5]
1C24 2A738D LD HL,[PARAM_AREA+3]
1C27

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LOCATION OBJECT CODE LINE SOURCE LINE
1C27 F5 6195 PUSH AF
1C28 FE00 6196 * IF (TABLE_CODE = SPRITE_NAME_TABLE) AND (MUX_SPRITES = TRUE) THEN
1C2A 2022 6197 CP 0
1C2C 3A73C7 6198 JR NZ,ELSEZZ
1C2F FE01 6199 LD A,(MUX_SPRITES)
1C31 2018 6200 CP 1
1C31 2018 6201 JR NZ,ELSEZZ
1C31 2018 6202 JR NZ,ELSEZZ
1C33 F1 6203 * WRITE ENTRY TO LOCAL TABLE
1C34 E5 6204 POP AF ; CLEAR STACK
1C34 E5 6205 PUSH HL ; [SP] = DATA BUFFER
1C35 2A8002 6206
1C38 78 6207 LD HL,(LOCAL_SPR_TBL) ; CALCULATE ADDRESS IN TABLE
1C39 CB27 6208 LD A,E
1C39 CB27 6209 SLA A
1C39 CB27 6210 SLA A
1C39 CB27 6211 LD E,A
1C3E 19 6212 ADD HL,DE
1C3F EB 6213 EX DE,HL
1C3F EB 6214
1C40 FDE5 6215 PUSH IY
1C42 C1 6216 POP BC
1C43 79 6217 LD A,C
1C44 CB27 6218 SLA A
1C46 CB27 6219 SLA A
1C48 4F 6220 LD C,A
1C49 E1 6221
1C4A ED80 6222 POP HL
1C4A ED80 6223 LDIR
1C4C 1807 6224
1C4C 1807 6225 JR END_IFZZ
1C4E 6226 * ELSE
1C4E F1 6227 ELSEZZ
1C4F CD1BAA 6228 POP AF
1C52 CD1D01 6229 CALL SET_COUNT
1C52 CD1D01 6230 CALL VRAM_WRITE
1C55 C9 6231 * END IF
1C55 C9 6232 END_IFZZ
1C55 C9 6233 RET
1C55 C9 6234
1C55 C9 6235
1C56 00010001 6236 * PROCEDURE INIT_SPR_ORDERQ (SPRITE_COUNT:BYTE);
1C56 00010001 6237
1C56 00010001 6238 * THIS IS THE PASCAL ENTRY POINT TO THE INIT_SPR_ORDER_ROUTINE.
1C56 00010001 6239
1C56 00010001 6240 INIT_SPR_P DEFW 1,1
1C56 00010001 6241
1C56 00010001 6242 INIT_SPR_ORDERQ EQU $
1C56 00010001 6243 BC,INIT_SPR_P
1C56 00010001 6244 DE,PARAM_AREA
1C56 00010001 6245 PARAM
1C56 00010001 6246 A,(PARAM_AREA)
1C56 00010001 6247
1C56 00010001 6248 INIT_SPR_ORDER_
1C56 00010001 6249 ;INITIALIZES THE SPRITE DISPLAY ORDER
1C56 00010001 6250 ;LIST IN RAM TO DEFAULT ORDER (0...31)
1C56 00010001 6251 ;IN: NUMBER OF SPRITES TO ORDER IN 'A'

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LOCATION OBJECT CODE LINE SOURCE LINE

```

1C66 47          LD B,A          ;SAVE SPRITE COUNT
1C67 AF          XOR A
1C68 2A0004      LD HL,(SPRITE_ORDER)
1C68 77          LD (HL),A
1C6C 23          INC HL
1C6D 3C          INC A
1C6E BA          CP B
1C6F 20FA       JR NZ,INIT_SPR10
1C71 C9          RET

6262
6263 * PROCEDURE WR_SPR_NM_TBLQ (SPRITE_COUNT:BYTE);
6264
6265 * THIS IS THE PASCAL ENTRY POINT TO THE WR_SPR_NM_TBL_ROUTINE.
6266
6267 WR_SPR_P     DEFM 1,1
6268
6269 WR_SPR_NM_TBLQ EQU $
6270 LD BC,WR_SPR_P
6271 LD DE,PARAM_AREA
6272 CALL PARAM
6273 LD A,(PARAM_AREA)
6274
6275 WR_SPR_NM_TBL_
6276
6277
6278
6279
6280 ;MODE_0_PORT EQU 0BEH
6281 ;MODE_1_PORT EQU 0BFH
6282
6283
6284 ;
6285
6286
6287
6288
6289
6290
6291
6292
6293
6294
6295
6296
6297
6298
6299 OUTPUT_LOOP_TABLE_MA
6300 LD HL,(LOCAL_SPR_TBL) ;RESTORE RAM SPRITE_NAME_TABLE POINTER
6301 LD C,[(IX+0)] ;DISPLAY ORDER
6302 INC IX
6303 LD B,0
6304 ADD HL,BC
6305 ADD HL,BC
6306 ADD HL,BC
6307 ADD HL,BC
6308
1C82
1C82 D02A8004
1C86 F5
1C87 FD2173F2
1C88 FD5E00
1C8E FD5601

1C91 7B
1C92 D38F
1C94 7A
1C95 F640
1C97 D38F

1C99 F1
1C9A
1C9A 2A8002
1C9D D04E00
1CA0 D023
1CA2 0600
1CA4 09
1CA5 09
1CA6 09
1CA7 09
    
```

;WRITES SPRITE NAME TABLE TO VRAM
;USING THE SPRITE ORDER LIST.
; ; NUMBER OF SPRITES TO WRITE IN 'A'

IX,[SPRITE_ORDER] ;LIST OF DISPLAY ORDERS
[SPRITE_CT],A ;SAVE COUNT
PUSH AF ;SAVE COUNT
LD IY,VRAM_ADDR_TABLE
LD E,[(IY+0)]
LD D,[(IY+1)] ;VRAM SPRITE_NAME_TABLE ADDRESS NOW IN DE.

;** SET UP VDP TO RECEIVE DATA

POP AF ;RESTORE COUNT

RESTORE RAM SPRITE_NAME_TABLE POINTER
DISPLAY ORDER
ADVANCE DISPLAY ORDER POINTER

```

LOCATION OBJECT CODE LINE SOURCE LINE
6309 ;** OUTPUT TO VRAM THROUGH VDP
6310
1CAB 0604 LD B,4 ;ELEN. CT COUNT FOR ONE SPRITE
1CAA 0E0E LD C,MODE_0_PORT ;OUTPUT PORT IN 'C'
1CAC OUTPUT_LOOP10
6313 OUTI
6314 MOP
6315 MOP ;DELAY
1CAE 00 MOP
1CAF 00 MOP
1CB0 20FA MOP
6317 JR NZ,OUTPUT_LOOP10
6318 LD A,[SPRITE_CT]
6319 DEC A
6320 LD [SPRITE_CT],A
6321 DEC A
6322 JR NZ,OUTPUT_LOOP_TABLE_MA
1CB2 30 RET
1CB3 20E5
1CB5 C9
6324
6325 GLB VRAM_ADDR_TABLE
6326 GLB SPRITENAMETBL
6327 GLB SPRITEGENTBL
6328 GLB PATIRNAMETBL
6329 GLB PATIRNGENTBL
6330 GLB COLORTABLE
6331 DATA
6332 VRAM_ADDR_TABLE
6333 SPRITENAMETBL DEFS 2
6334 SPRITEGENTBL DEFS 2
6335 PATIRNAMETBL DEFS 2
6336 PATIRNGENTBL DEFS 2
6337 COLORTABLE DEFS 2
6338 * THIS TABLE HOLDS THE BASE ADDRESSES OF ALL THE VRAM TABLES.
6339
6340
6341 SAVE_TEMP DEFS 2
6342 SAVED_COUNT DEFS 2
6343
6344 ;
6345 COMM
6346 ;PARAM AREA DEFS 6
6347 * THIS IS THE PARAMETER PASSING AREA FOR THE PASCAL ENTRY POINTS TO
6348 * ROUTINES IN THIS MODULE. IT IS HELD IN COMMON WITH OTHER SUCH ENTRY
6349 * POINTS FOR OTHER MODULES.
6350 PROG

```

OCATION OBJECT CODE LINE SOURCE LINE

6465 * THIS IS THE PARAMETER DESCRIPTOR FOR REG_WRITED

```

6466
6467 * BEGIN REG_WRITE
6468 GLB REG_WRITE
6469 EQU $
6470 LD BC,REG_WRITE_P
6471 LD DE,PARAM_AREA
6472 CALL PARAM
6473 LD HL,[PARAM_AREA]
6474 LD C,H
6475 LD B,L
6476
6477 GLB REG_WRITE
6478 EQU $
6479
6480 * VALUE =; CTRL_PORT
6481 LD A,C
6482 OUT CTRL_PORT],A
6483
6484 * REGISTER + 80H =; CTRL_PORT
6485 LD A,B
6486 ADD A,80H
6487 OUT CTRL_PORT],A
6488
6489 * IF REGISTER = 0 THEN VDP_MODE_WORD[0] := VALUE
6490 LD A,B
6491 CP 0
6492 JR NZ,NOT_REG_0
6493 LD A,C
6494 LD [VDP_MODE_WORD],A
6495 NOT_REG_0 EQU $
6496
6497 * IF REGISTER = 1 THEN VDP_MODE_WORD[1] := VALUE
6498 LD A,B
6499 CP 1
6500 JR NZ,NOT_REG_1
6501 LD A,C
6502 LD [VDP_MODE_WORD+1],A
6503 NOT_REG_1 EQU $
6504
6505 * END REG_WRITE
6506 RET
6507
6508 * PROCEDURE VRAM_WRITE (VAR DATA:BUFFER;DEST:INTEGER;COUNT:INTEGER)
6509
6510 * VAR DATA (POINTER TO DATA BUFFER) IS PASSED IN HL
6511 * DEST IS PASSED IN DE
6512 * COUNT IS PASSED IN BC
6513 * DESTROYS: ALL
6514
6515 VRAM_WRITE_P DEFW 3,-2,2,2
6516 * THIS IS THE PARAMETER DESCRIPTOR FOR VRAM_WRITED
6517
6518 * BEGIN VRAM_WRITE
6519 GLB VRAM_WRITED
6520 EQU $
6521
6522 * END VRAM_WRITE
6523
6524 * END PROCEDURE
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6998 * END OF FILE
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7000 * END OF PROGRAM
    
```

LOCATION	OBJECT CODE	LINE	SOURCE	LINE
1CE0	011CE5	6521	LD	BC,VRAM_WRITE_P
1CF0	11738A	6522	LD	DE,PARAM_AREA
1CF3	CD0098	6523	CALL	PARAM
1CF6	2A738A	6524	LD	HL,(PARAM_AREA)
1CF9	ED58738C	6525	LD	DE,(PARAM_AREA+2)
1CFD	ED4B738E	6526	LD	BC,(PARAM_AREA+4)
		6527		
	<1D01>	6528	GLB	VRAM_WRITE
		6529	EQU	\$
		6530		
1D01	E5	6531	* DEST := DEST + 4000H	
1D02	D5	6532	PUSH	HL
1D03	E1	6533	PUSH	DE
1D04	114000	6534	POP	HL
1D07	19	6535	LD	DE,4000H
		6536	ADD	HL,DE
		6537		
1D08	7D	6538	* LOW BYTE OF DEST := CTRL_PORT	
1D09	D3BF	6539	LD	A,L
		6540	OUT	[CTRL_PORT],A
		6541		
1D08	7C	6542	* HIGH BYTE OF DEST := CTRL_PORT	
1D0C	D3BF	6543	LD	A,H
		6544	OUT	[CTRL_PORT],A
		6545		
1D0E	C5	6546	* DATA := DATA_PORT	
1D0F	D1	6547	PUSH	BC
1D10	E1	6548	POP	DE
1D11	DEBE	6549	POP	HL
1D13	43	6550	LD	C,DATA_PORT
		6551	LD	B,E
		6552	EQU	\$
1D14	EDA3	6553	OUTPUT_LOOP	
1D16	00	6554	MOP	NZ,OUTPUT_LOOP
1D17	00	6555	MOP	D
1D18	C21D14	6556	JP	M,END_OUTPUT
1D18	15	6557	DEC	NZ,OUTPUT_LOOP
1D1C	FA1D21	6558	JP	
1D1F	20F3	6559	JR	
		6560		
1D21	C9	6561	* END VRAM_WRITE	
	<1D21>	6562	EQU	\$
		6563	RET	
		6564		
1D22	0003FFFE	6565	* PROCEDURE VRAM_READ (VAR DATA:BUFFER;SRCE:INTEGER;COUNT:INTEGER)	
1D26	00020002	6566		
		6567	* VAR DATA (POINTER TO DATA BUFFER) IS PASSED IN HL	
		6568	* SRCE IS PASSED IN DE	
		6569	* COUNT IS PASSED IN BC	
		6570	* DESTROYS: ALL	
		6571		
		6572	VRAM_READ_P	DEFW 3,2,2,2
		6573	* THIS IS THE PARAMETER DESCRIPTOR FOR VRAM_READ	
		6574		
		6575	* BEGIN VRAM_READ	
		6576	GLB	VRAM_READ

```

LOCATION OBJECT CODE LINE SOURCE LINE
<102A> 6577 VRAM_READQ EQU $
102A 011D22 LD BC,VRAM_READ_P
102D 1173BA LD DE,PARAM_AREA
1030 CD0098 CALL PARAM
1033 2A73BA LD HL,(PARAM_AREA)
1036 ED5873BC LD DE,[PARAM_AREA+2]
103A ED4873BE LD BC,[PARAM_AREA+4]
6584 6585 GLB VRAM_READ
6586 VRAM_READ EQU $
<103E> 6587
6588 * LOW BYTE OF SRCE =; CTRL_PORT A,E
6589 LD [CTRL_PORT],A
103F D3BF OUT
6591 * HIGH BYTE OF SRCE =; CTRL_PORT A,D
6592 LD [CTRL_PORT],A
6593 OUT
6594
6595 * DATA =; DATA_PORT BC
6596 LD [DATA_PORT],BC
6597 PUSH DE
6598 POP C,DATA_PORT
1044 C5 LD B,E
1045 D1 LD $
1046 0E8E LD $
1048 43 EQU $
<1049> 6601 INPUT_LOOP EQU $
1049 EDA2 INI MZ,INPUT_LOOP
104B 00 MOP D
104C 00 MOP M,END_INPUT
104D C21049 JP MZ,INPUT_LOOP
1050 15 DEC
1051 FA1D56 JP
1054 20F3 JR
6608
6609 * END VRAM_READ EQU $
6610
6611 END_INPUT EQU RET
1056 C9
6612
6613 * FUNCTION REG_READ:BYTE
6614
6615 * FUNCTION OUTPUT RETURNED IN A
6616
6617 * DESTROYS A ONLY
6618
6619 * BEGIN REG_READ GLB REG_READ
6620 EQU $
6621 REG_READ
6622
6623 * REG_READ ;= CTRL_PORT IN A,[CTRL_PORT]
6624
6625 * END REG_READ RET
1057 DBBF
6626
6627
6628
6629 PROG
    
```

LOCATION OBJECT CODE LINE SOURCE LINE

6631 * THIS IS A PACKAGE OF ROUTINES THAT ALLOW APPLICATIONS PROGRAMMERS TO
 6632 * OPERATE ON SHAPE GENERATORS. EACH OF THEM TAKES, AS INPUTS, AN AREA
 6633 * IN ONE OF THE GENERATOR TABLES IN WHICH THE GENERATORS TO BE OPERATED
 6634 * UPON RESIDE, A COUNT OF THE GENERATORS TO BE USED, AND AN AREA OF THE
 6635 * SAME TABLE INTO WHICH THE RESULTS ARE TO BE PUT. THE ONLY RAM AREA THEY
 6636 * IS IN THE WORK_BUFFER A POINTER TO WHICH IS DECLARED AS AN EXTERNAL
 6637 * AND DEFINED IN THE CARTRIDGE.
 6638 *
 6639

6640 ***** NOTE: *****
 6641 THESE ROUTINES WRITE TO AND READ *****
 6642 WITHOUT POSSIBILITY OF DEFERAL *****
 6643 AND SHOULD NOT BE USED IN ANY *****
 6644 SITUATION WHERE THEY MAY BE *****
 6645 INTERRUPTED. *****
 6646 *****
 6647

6648 ; EXT WORK_BUFFER
 6649 ; POINTER TO THE WORK_BUFFER DEFINED BY THE CARTRIDGE PROGRAMMER
 6650

6651 ; EXT VDP_MODE_WORD
 6652 ; THE WORD IN OS RAM THAT DESCRIBES THE CURRENT GRAPHICS MODE.
 6653

6654 ; EXT GET_VRAM
 6655 ; EXT PUT_VRAM
 6656 * EXTERNAL OS ROUTINES IN TABLE_MANAGER MODULE
 6657

6658 ; EXT MIRROR_LR
 6659 ; EXT MIRROR_UD
 6660 ; EXT ROTATE
 6661 ; EXT MAGNIFY
 6662 ; EXT QUADRUPLE
 6663 * EXTERNAL ROUTINES THAT PERFORM BLOCK OPERATIONS
 6664

6665 ;TRUE EQU 1
 6666 ;FALSE EQU 0
 6667 * VALUES FOR BOOLEAN FLAGS
 6668

6669 PATTERN_GEN EQU 3
 6670 COLOR_TABLE EQU 4
 6671 * VALUES FOR TABLE CODE
 6672

6673 * PROCEDURE REFLECT_VERTICAL (TABLE_CODE(A),SOURCE(DE),DESTINATION(HL),COUNT(BC))
 6674

6675 * REFLECT REFLECTS EACH OF A BLOCK OF GENERATORS FROM VRAM AROUND
 6676 * THE VERTICAL AXIS. IF THE GENERATORS ARE FROM THE PATTERN PLANE
 6677 * AND THE GRAPHICS MODE IS 2, THEN THE ROUTINE ALSO COPIES THE
 6678 * CORRESPONDING COLOR GENERATORS. OTHERWISE IS ASSUMES THAT THE COLOR
 6679 * DATA HAS ALREADY BEEN SET UP.
 6680

6681 * BEGIN REFLECT_VERTICAL
 6682 ; RFLCT_VERT
 6683 ; ACTUAL ROUTINE NAME EXISTS IN OS
 6684 ; JUMP TABLE ONLY
 6685

6686 * SET OPERATION CODE
 6687 ; LD IX,RFLCT_VERT

105A

105A 00211096

```

LOCATION OBJECT CODE LINE SOURCE LINE
105E 1810
6688 * CONTINUE BELOW
6689 * CONTINUE BELOW JR CONTINUE_GRAPHICS
6690
6691
6692
6693 * PROCEDURE REFLECT_HORIZONTAL (TABLE_CODE(A),SOURCE(DE),DESTINATION(HL),COUNT(BC))
6694
6695 * REFLECT_HORIZONTAL REFLECTS EACH OF A BLOCK OF GENERATORS FROM VRAM
6696 * AROUND THE HORIZONTAL AXIS. IF THE GENERATORS ARE FROM THE PATTERN
6697 * PLANE AND THE GRAPHICS MODE IS 2 THEN IT REFLECTS THE CORRESPONDING
6698 * COLOR GENERATORS AS WELL.
6699
6700 * BEGIN REFLECT_HORIZONTAL
6701 * REFLECT_HORIZONTAL
6702 * REFLECT_HORIZONTAL
6703 * REFLECT_HORIZONTAL
6704
6705 * SET OPERATION CODE
6706 * LD
6707 * LD
6708 * CONTINUE BELOW JR CONTINUE_GRAPHICS
6709
6710
6711
6712
6713
6714 * PROCEDURE ROTATE_90 (TABLE_CODE(A),SOURCE(DE),DESTINATION(HL),COUNT(BC))
6715
6716 * ROTATE_90 ROTATES EACH OF A BLOCK OF GENERATORS FROM VRAM 90 DEGREES
6717 * CLOCKWISE. IF THE GENERATORS ARE FROM THE PATTERN PLANE AND THE
6718 * GRAPHICS MODE IS 2 THEN THE ROUTINE COPIES THE CORRESPONDING COLOR
6719 * ENTRIES AS WELL.
6720
6721 * BEGIN ROTATE_90
6722 * ROTATE_90
6723 * ROTATE_90
6724
6725
6726 * SET OPERATION CODE
6727 * LD
6728 * LD
6729 * CONTINUE BELOW JR CONTINUE_GRAPHICS
6730
6731
6732
6733
6734
6735 * PROCEDURE ENLARGE (TABLE_CODE(A),SOURCE(DE),DESTINATION(HL),COUNT(BC))
6736
6737 * ENLARGE TAKES EACH OF A BLOCK OF GENERATORS AND ENLARGES IT INTO
6738 * A BLOCK OF FOUR GENERATORS WHEREIN EACH PIXEL OF THE ORIGINAL
6739 * GENERATOR IS EXPANDED TO FOUR PIXELS IN THE NEW ONES. IF THE
6740 * GENERATORS ARE FROM THE PATTERN PLANE AND THE GRAPHICS MODE IS 2
6741 * THE ROUTINE ALSO QUADRUPLES EACH OF THE CORRESPONDING COLOR
6742 * GENERATORS AS WELL.
6743
6744 * BEGIN ENLARGE

```

LOCATION	OBJECT CODE LINE	SOURCE LINE	GLB	EMLRG	;	ACTUAL ROUTINE NAME EXISTS IN OS	;	JUMP TABLE ONLY
106C	6745							
	6746	EMLRG						
	6747							
	6748							
	6749							
106C D0211E07	6750 *	SET OPERATION CODE						
	6751	LD						IX,EMLRG_
	6752							
	6753							
1070	6754 *	CONTINUE EXECUTION HERE						
	6755	CONTINUE_GRAPHICS						
	6756							
	6757 *	SAVE ALL ENTRY PARAMETERS						
1070 D9	6758	EXX						AF,AF'
1071 08	6759	EX						IX
1072 D0E5	6760	PUSH						; (SP) = OPERATION CODE
	6761							
	6762 *	REPEAT						
1074	6763	MAIN_LOOP						
	6764							
	6765 *	GET_VRAM_ (TABLE_CODE,SOURCE,WORK_BUFFER(0...7),1)						
	6766	EX						AF,AF'
1074 08	6767	PUSH						AF
1075 F5	6768	EX						AF,AF'
1076 08	6769	POP						AF
1077 F1	6770	EXX						DE
1078 D9	6771	PUSH						DE
1079 D5	6772	EXX						IX,1
107A D9	6773	POP						HL,(WORK_BUFFER)
107B D1	6774	LD						GET_VRAM_
107C F0210001	6775	LD						
1080 2A8006	6776	CALL						
1083 CD1BA3	6777							
	6778 *	EXECUTE ENCODED OPERATION BELOW						
1086 D0E1	6779	POP						
1088 D0E5	6780	PUSH						
108A D0E9	6781	JP						[IX]
	6782	RETURN_HERE						\$
	6783							
108C 13	6784 *	SOURCE : SUCC (SOURCE)						DE
	6785	INC						
	6786							
	6787 *	COUNT := PRED (COUNT)						BC
	6788	DEC						
108D 08	6789							
	6790 *	UNTIL COUNT = 0						
108E 78	6791	LD						A,B
108F 81	6792	OR						C
1090 D9	6793	EXX						
1091 20E1	6794	JR						NZ,MAIN_LOOP
	6795							
	6796 *	END (ALL)						
1093	6797	ALL_X						
1093 D0E1	6798	POP						IX ; CLEAR STACK
1095 C9	6799	RET						
	6800							
1096	6801	R1LCT VERT						

LOCATION	OBJECT CODE LINE	SOURCE LINE
	6802 *	OPERATIONS SPECIFIC TO REFLECT_VERTICAL ROUTINE
	6803	
1096 2A8006	6804 *	MIRROR_L_R(WORK_BUFFER(0..7),WORK_BUFFER(8..15))
1099 010000	6805	LD HL,(WORK_BUFFER)
109C E5	6806	LD BC,8
	6807	HL
1090 D1	6808	PUSH DE
109E 09	6809	POP HL,BC
109F EB	6810	EX DE,HL
10A0 CD1F00	6811	CALL MIRROR_L_R
	6812	
10A3 CD1E72	6813 *	PUT_VRAM(TABLE_CODE,DESTINATION,WORK_BUFFER(8..15),1)
	6814	CALL PUT_TABLE
	6815	
10A6 CD1E50	6816 *	IF COLOR_TEST THEN
10A9 FE01	6817	CALL COLOR_TEST
10AB 2006	6818	CP TRUE
	6819	JR NZ,END_IF_1_GRAPHICS
	6820	
10AD CD1E09	6821 *	GET_VRAM(COLOR_TABLE,SOURCE,WORK_BUFFER(0..7),1)
	6822	CALL GET_COLOR
	6823	
10B0 CD1E9A	6824 *	PUT_VRAM(COLOR_TABLE,DESTINATION,WORK_BUFFER(0..7),1)
	6825	CALL PUT_COLOR
	6826	
10B3	6827 *	END_IF
	6828	END_IF_1_GRAPHICS
	6829	
10B3 D9	6830 *	DESTINATION := SUCC(DESTINATION)
10B4 23	6831	EXX
	6832	INC HL
	6833	
10B5 1B05	6834 *	END
	6835	JR RETURN_HERE
	6836	
	6837	
10B7	6838	
	6839	REFLCT_HOR
	6840 *	OPERATIONS SPECIFIC TO REFLECT_HORIZONTAL ROUTINE
	6841	
10B7 2A8006	6842 *	MIRROR_U_D(WORK_BUFFER(0..7),WORK_BUFFER(8..15))
10BA 010000	6843	LD HL,(WORK_BUFFER)
10B0 E5	6844	LD BC,8
10BE D1	6845	PUSH HL
10BF D9	6846	POP DE
10C0 EB	6847	ADD HL,BC
10C1 CD1F4E	6848	EX DE,HL
	6849	CALL MIRROR_U_D
	6850	
10C4 CD1E72	6851 *	PUT_VRAM(TABLE_CODE,DESTINATION,WORK_BUFFER(8..15),1)
	6852	CALL PUT_TABLE
	6853	
10C7 CD1E50	6854 *	IF COLOR_TEST THEN
10CA FE01	6855	CALL COLOR_TEST
10CC 2013	6856	CP TRUE
	6857	JR NZ,END_IF_2_GRAPHICS

LOCATION	OBJECT CODE	LINE	SOURCE LINE
1DCE	CD1EB9	6859 *	GET_VRAM(COLOR_TABLE,SOURCE,WORK_BUFFER[0..7],1)
		6860	CALL GET_COLOR
		6861	
1D01	2A8006	6862 *	MIRROR_U_D(WORK_BUFFER[0..7],WORK_BUFFER[8..15])
1D04	010008	6863	LD HL,(WORK_BUFFER)
1D07	E5	6864	BC,B
1D08	D1	6865	PUSH HL
1D09	09	6866	POP DE
1D0A	E8	6867	ADD HL,BC
1D0B	CD1F4E	6868	DE,HL
		6869	CALL MIRROR_U_D
		6870	
1D0E	CD1E9A	6871 *	PUT_VRAM(COLOR_TABLE,DESTINATION,WORK_BUFFER[8..15],1)
		6872	CALL PUT_COLOR
		6873	
1DE1		6874 *	END IF
		6875	END_IF_2_GRAPHICS
		6876	
1DE1	D9	6877 *	DESTINATION := SUCC (DESTINATION)
1DE2	Z3	6878	EXX
		6879	INC HL
		6880	
1DE3	1BA7	6881 *	END
		6882	JR RETURN_HERE
		6883	
		6884	
		6885	
1DE5		6886	ROT 90
		6887 *	OPERATIONS SPECIFIC TO THE ROTATE_90 ROUTINE
		6888	
		6889	
1DE5	2A8006	6890 *	ROTATE(WORK_BUFFER[0..7],WORK_BUFFER[8..15])
1DEB	010008	6891	LD HL,(WORK_BUFFER)
1DEB	E5	6892	BC,B
1DEC	D1	6893	PUSH HL
1DED	09	6894	POP DE
1DEE	E8	6895	ADD HL,BC
1DEF	CD1F12	6896	DE,HL
		6897	CALL ROTATE
		6898	
1DF2	CD1E72	6899 *	PUT_VRAM (TABLE_CODE,DESTINATION,WORK_BUFFER[8..15],1)
		6900	CALL PUT_TABLE
		6901	
1DF5	CD1E5D	6902 *	IF COLOR_TEST THEN
1DF8	FE01	6903	CALL COLOR_TEST
1DFA	2006	6904	CP TRUE
		6905	JR MZ,END_IF_3_GRAPHICS
		6906	
1DFC	CD1EB9	6907 *	GET_VRAM(COLOR_TABLE,SOURCE,WORK_BUFFER[0..7],1)
		6908	CALL GET_COLOR
		6909	
1DFF	CD1E9A	6910 *	PUT_VRAM(COLOR_TABLE,DESTINATION,WORK_BUFFER[0..7],1)
		6911	CALL PUT_COLOR
		6912	
1E02		6913 *	END IF
		6914	END_IF_3_GRAPHICS
		6915	

OBJECT CODE	LINE	SOURCE LINE
1E02 D9	6916 *	DESTINATION := SUCC (DESTINATION)
1E03 23	6917	EXX
	6918	INC HL
	6919	HL
1E04 C3108C	6920 *	EMD
	6921	JP RETURN_HERE
	6922	
	6923	
	6924	
1E07	6925	EMLRG
	6926 *	OPERATIONS SPECIFIC TO THE ENLARGE ROUTINE
	6927	
1E07 2AB006	6928 *	MAGNIFY(WORK_BUFFER(0..7),WORK_BUFFER(0..39))
1E0A 010008	6929	LD HL,(WORK_BUFFER)
1E00 E5	6930	LD BC,0
1E0E D1	6931	PUSH HL
1E0F 09	6932	POP DE
1E10 EB	6933	ADD HL,BC
1E11 CD1EAB	6934	EX DE,HL
	6935	CALL MAGNIFY
	6936 *	PUT_VRAM (TABLE_CODE,DESTINATION,WORK_BUFFER(0..39),4)
1E14 08	6937	EX AF,AF'
1E15 F5	6938	PUSH AF
1E16 08	6939	EX AF,AF'
1E17 F1	6940	POP AF
1E18 D9	6941	EXX HL
1E19 E5	6942	PUSH HL
1E1A D9	6943	EXX DE
1E1B D1	6944	POP HL,(WORK_BUFFER)
1E1C 2AB006	6945	LD BC,0
1E1F 010008	6946	LD HL,BC
1E22 09	6947	ADD LY,4
1E23 FD210004	6948	LD PUT_VRAM
1E27 CD1C27	6949	CALL
	6950	
	6951	
1E2A CD1E50	6952 *	IF COLOR_TEST THEN
1E2D FE01	6953	CALL COLOR_TEST
1E2F 2024	6954	CP TRUE
	6955	JR NZ,END_IF_4_GRAPHICS
	6956	
1E31 CD1E89	6957 *	GET_VRAM(COLOR_TABLE,SOURCE,WORK_BUFFER(0..7),1)
	6958	CALL GET_COLOR
	6959	
1E34 2AB006	6960 *	QUADRUPLE(WORK_BUFFER(0..7),WORK_BUFFER(0..39))
1E37 010008	6961	LD HL,(WORK_BUFFER)
1E3A E5	6962	LD BC,0
1E3B D1	6963	PUSH HL
1E3D 01	6964	POP DE
1E3E EB	6965	ADD HL,BC
1E3F 09	6966	EX DE,HL
1E40 EB	6967	CALL QUADRUPLE
1E41 3E04	6968	
1E42	6969 *	PUT_VRAM(COLOR_TABLE,DESTINATION,WORK_BUFFER(0..39),4)
1E44 E5	6970	LD A,COLOR_TABLE
	6971	EXX HL
	6972	PUSH HL