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1 This file contains the logic necessary for a GAL22V10 to latch data bits
2 D0-D4 and multiplex the lowest three and the uppermost address lines
3 (MA0, MA1, MA2, and MA8) to the 256K SIMMs on the Micro Innovations
4 1MB/2MB memory board.
5
6
7 GAL16V8 1: A3, 2: A11, 3: A4, 4: A12, 5: A5, 6: A13,
8 7: A6, 8: A14, 9: A7, 11: A15, 12: MA3, 13: MA4,
9 14: MA5, 15: MA6, 16: MA7, 17: RAS, 18: RS0, 19: MUX
10
11
12 MA3 = (A3 & MUX') + (A11 & MUX)
13 MA4 = (A4 & MUX') + (A12 & MUX)
14 MA5 = (A5 & MUX') + (A13 & MUX)
15 MA6 = (A6 & MUX') + (A14 & MUX)
16 MA7 = (A7 & MUX') + (A15 & MUX)
17 RS0 = RAS
18
19 Signature: "2MU3rev0"
I289 Simple GAL architecture selected.
[Complex mode cannot be used because macrocell 0 feeds into the
array. Registered mode cannot be used because (a) the clock line is
used as input, and (b) the enable line is used as input.]

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#### RESOLVED EXPRESSIONS (Reduction 2)

Signal name	Row	Terms
MA3	56	A3 MUX'
	57	A11 MUX
MA4	48	A4 MUX'
	49	A12 MUX
MA5	40	A5 MUX'
	41	A13 MUX
MA6	32	A6 MUX'
	33	A14 MUX
MA7	24	A7 MUX'
	25	A15 MUX
RS0	8	RAS

#### ♀ SIGNAL ASSIGNMENT

Pin	Signal name	Column	Rows			Activity
			Beg	Avail	Used	
1.	A3	2	-	-	-	High (Clock)
2.	A11	0	-	-	-	
3.	A4	4	-	-	-	
4.	A12	8	-	-	-	
5.	A5	12	-	-	-	

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2MU3V8. LST						(Enable)
6.	A13	16	-	-	-	
7.	A6	20	-	-	-	
8.	A14	24	-	-	-	
9.	A7	28	-	-	-	
11.	A15	30	-	-	-	
12.	MA3	26	56	8	2	
13.	MA4	22	48	8	2	
14.	MA5	18	40	8	2	
15.	MA6	0	32	8	2	
16.	MA7	0	24	8	2	Hi gh
17.	RAS	14	16	8	0	
18.	RS0	10	8	8	1	
19.	MUX	6	0	8	0	
			----	----		
			64	11	(17%)	

I 200 No fatal errors found in source code.  
I 201 No warnings.

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♀OrCAD PLD
Type:      GAL16V8
*
QP20* QF2194* QV1024*
F0*
L0256 11 11 11 11 11 11 11 11 01 11 11 11 11 11 11 11 *
L0768 11 11 11 10 11 11 11 11 11 11 11 11 11 11 01 11 *
L0800 11 11 11 01 11 11 11 11 11 11 11 11 11 11 11 01 *
L1024 11 11 11 10 11 11 11 11 11 11 01 11 11 11 11 11 *
L1056 11 11 11 01 11 11 11 11 11 11 11 11 01 11 11 11 *
L1280 11 11 11 10 11 11 11 01 11 11 11 11 11 11 11 11 *
L1312 11 11 11 01 11 11 11 11 01 11 11 11 11 11 11 11 *
L1536 11 11 01 10 11 11 11 11 11 11 11 11 11 11 11 11 *
L1568 11 11 11 01 01 11 11 11 11 11 11 11 11 11 11 11 *
L1792 11 01 11 10 11 11 11 11 11 11 11 11 11 11 11 11 *
L1824 01 11 11 01 11 11 11 11 11 11 11 11 11 11 11 11 *
L2048 11 11 11 11 00 11 00 10 01 00 11 01 01 01 01 01 *
L2080 00 11 00 11 01 11 00 10 01 10 01 01 01 11 01 10 *
L2112 00 11 00 00 10 10 00 00 11 11 11 11 11 11 11 11 *
L2144 11 11 11 11 11 11 11 11 11 11 11 11 11 11 11 11 *
L2176 11 11 11 11 11 11 11 11 10 *
C342B*

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I 202 1/31/92 9: 50 pm (Fri day)  
I 203 Memory utilizati on 2069/22374 (9%)  
I 204 El apsed time 3 seconds

♀  
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